

**Notice of References Cited**

Application/Control No.

10/075,218

Applicant(s)/Patent Under

Reexamination

KOBAYASHI ET AL.

Examiner

Steven J. Fulk

Art Unit

2891

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**U.S. PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	Wolf et al., "Silicon Processing for the VLSI Era: Process Integration," Vol. II, 1990, pp. 273-275.
V	
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a))  
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